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PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10083611	FILING DATE 02/27/2002	CLASS 257	SUBCLASS 316	GAU 2812	EXAMINER Boyle
**APPLICANTS: Hisamune Yoshiaki; Nakata Hidetoshi;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/489,332 01/21/2000 PAT 6,373,096					
BEST AVAILABLE COPY					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 11-014707 01/22/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 8020-1002-1	
TITLE : Method of manufacturing semiconductor device, nonvolatile semiconductor memory device and method of manufacturing the same					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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